

1	2	3	4	5	6	7	8
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A



F

G

- H

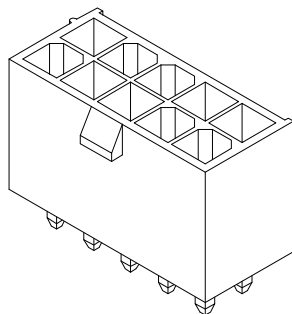
1	2	3	4	5	6	7	8
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Circuit	Dimensions (mm)		Circuit	Dimensions (mm)	
	A	B		A	B
02	---	5.40	14	25.20	30.60
04	4.20	9.60	16	29.40	34.80
06	8.40	13.80	18	33.60	39.00
08	12.60	18.00	20	37.80	43.20
10	16.80	22.20	22	42.00	47.40
12	21.00	26.40	24	46.20	51.60

REV.	REVISION RECORD	DATE		GENERAL TOLERANCES		SCALE: 1:1	NAME	DATE	PART.NO: C42011F0-2XX1XXX	DWG.NO: ENDE05	 WanLian Technology Co., Ltd.
AO	NEW RELEASE	21.11.17		LINEAR	ANGLES	APPROVED	Wang_jr	21.11.17			
				0.0±0.35	X*REF±6°						
			UNIT:mm	0.00±0.25	X*±3°	DESIGNER	Han_Gao	21.11.17	TITLE: Pitch 4.2mm 双排 180° DIP Wafer 实心针		
			SIZE: A4	0.000±0.10	X*X' ±2°	DRAWN	Zijun_Huang	21.11.17			
										REV: AO	SHEET: 1/2

The product using material and processing must conform to the "WL-PZ-001"HSF technical standard control requirements

RoHS



胶袋

2*1PIN:
胶袋规格:30*25CM

胶袋

2*2-2*12PIN:
胶袋规格:36.5*29CM

胶袋

2*9PIN:
胶袋规格:44*31CM

一、外箱

- PE袋

- 护板*4PCS

Material:K=K
MEAS:42*27*19

注意事项:

1. 袋装产品封口前需放1-2包干燥剂于内；
2. 产品不可少数、多数；
3. 内外包装均需有数量标签；
4. 不同PIN位包装时须分开放置不可装入同一外箱中。
5. 尾数包装需用不影响品质之物品填充并注明尾数。

2*1-2*3PIN:1000PCS/袋
2*4-2*9PIN:500PCS/袋
2*10-2*12PIN:200PCS/袋

1 Kpcs	0. 2Kpcs	2*12P
1 Kpcs	0. 2Kpcs	2*11P
1. 2 Kpcs	0. 2Kpcs	2*10P
1. 5Kpcs	0. 5Kpcs	2*9P
2. 0Kpcs	0. 5Kpcs	2*8P
2. 0Kpcs	0. 5Kpcs	2*7P
2. 0Kpcs	0. 5Kpcs	2*6P
3 Kpcs	0. 5Kpcs	2*5P
3. 5Kpcs	0. 5Kpcs	2*4P
5 Kpcs	1Kpcs	2*3P
8 Kpcs	1Kpcs	2*2P
10 Kpcs	1Kpcs	2*1P
PCS/Cartion	PCS/Bag	POS

REV.	REVISION RECORD	DATE		GENERAL TOLERANCES		SCALE: 1:1	NAME	DATE	PART.NO: C42011F0-2XX1XXX	DWG.NO: ENDE05	 WanLian Teconology Co., Ltd	
AO	NEW RELEASE	21.11.17		LINEAR	ANGLES	APPROVED	Wang_jr	21.11.17				
				0.0±0.35	X*REF±6°							
				UNIT:mm	0.00±0.25	X°±3°	DESIGNER	Han_Gao	21.11.17	TITLE: 包装图		
				SIZE: A4	0.000±0.10	X°X' ±2°	DRAWN	Zijun_Huang	21.11.17			
										REV: AO	SHEET: 2/2	

 万连科技
WanLian Teconology Co., Ltd

REV: A0	SHEET: 2/2
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